

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :	Budong You et al.	Art Unit :	2818
Serial No. :	10/713,749	Examiner :	Dung Anh Le
Filed :	November 13, 2003	Confirmation No.:	1619
		Notice of Allowance Date:	January 25, 2007
Title :	A METHOD OF FABRICATING A LATERAL DOUBLE-DIFFUSED MOSFET		

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

COMMENTS ON EXAMINER'S REASONS FOR ALLOWANCE

Applicant recognizes that in accordance with **M.P.E.P. § 1302.14**, the Examiner's reasons for allowance need not set forth all of the details as to why the claims are allowed. In the above-referenced application, Applicant does not concede that the Examiner's stated reasons for allowance are the only reasons for which the claims are allowable. In particular, Applicant does not concede that the identified limitation "implanting, into the drain region of the transistor, a fifth impurity region with a fifth volume and a fifth surface area, the fifth impurity region being of the first type" is necessary to distinguish the prior art of record or to satisfy the requirements of 35 U.S.C. § 112. The claims may be allowable for other reasons as well (i.e., on the basis of a combination of some of the features recited therein). In addition, the Examiner does not assert, and Applicant would not concede, that the Examiner's reasons have any bearing on the patentability of claims in any other applications directed to the disclosed subject matter.

In addition, each dependent claim stands on its own and may be allowable on its own merits. In particular, each dependent claim may be allowable on the basis of a combination of some of the features recited in the dependent claim and its base claim(s), which combination of features may not include all of the limitations identified in the Examiner's reasons for allowance.

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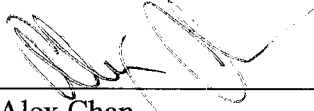
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Attorney's Docket No.: 09464-025001

Respectfully submitted,

Date: 2/13/07



Alex Chan
Reg. No. 52,713

Fish & Richardson P.C.
500 Arguello Street, Suite 500
Redwood City, California 94063
Telephone: (650) 839-5070
Facsimile: (650) 839-5071